L Number	Hits	Search Text	DB	Time stamp
1	1177	324/73.1.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/18 09:54
2	73	324/73.1.ccls. and (address adj1 (signal or bit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/18 10:18
3	627	(NMOS and PMOS) and (address adj1 bit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/18 10:20
4	377	((NMOS and PMOS) and (address adj1 bit)) and (measuring or measure or testing or test)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/18 10:21
5	150	(((NMOS and PMOS) and (address adj1 bit)) and (measuring or measure or testing or test)) and (test adj1 mode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/18 10:35
6	85	((((NMOS and PMOS) and (address adj1 bit)) and (measuring or measure or testing or test)) and (test adj1 mode)) and resistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/18 10:22
7	99	((((NMOS and PMOS) and (address adj1 bit)) and (measuring or measure or testing or test)) and (test adj1 mode)) and pad	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/18 10:29
8	140886	((((NMOS and PMOS) and (address adj1 bit)) and (measuring or measure or testing or test)) and (test adj1 mode)) ((receive or receiving or input or inoutting) with (address))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/18 10:38
9	140	((((NMOS and PMOS) and (address adj1 bit)) and (measuring or measure or testing or test)) and (test adj1 mode)) and((receive or receiving or input or inoutting) with (address))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/18 10:57
10	1	((((NMOS and PMOS) and (address adj1 bit)) and (measuring or measure or testing or test)) and (test adj1 mode)) and((indicat$ or display) with (value near6 pad))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/18 11:00
11	1	((((NMOS and PMOS) and (address adj1 bit)) and (measuring or measure or testing or test)) and (test adj1 mode)) and((indicating or indicative or displaying or display) with (value near6 pad))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/18 11:04
12	5	((((NMOS and PMOS) and (address adj1 bit)) and (measuring or measure or testing or test)) and (test adj1 mode)) and((indicating or indicative or displaying or display or indicate or output) with (value near6 pad))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/18 11:09
13	150	((((NMOS and PMOS) and (address adj1 bit)) and (measuring or measure or testing or test)) and (test adj1 mode)) and(indicating or indicative or displaying or display or indicate or output)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/18 11:10

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14	125	((((NMOS and PMOS) and (address adj1 bit)) and (measuring or measure or testing or test)) and (test adj1 mode)) and(indicating or indicative or displaying or display or indicate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/18 11:10